



Automatic Program Reoptimization Support in LLVM ORC JIT

by Sunho Kim





• Undergrad student from UC San Diego

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 - Which is what this talk will be about

- Compile with -O2 for only "hot" functions
 - The compilation time of -00 or -01 is faster than -02 in general

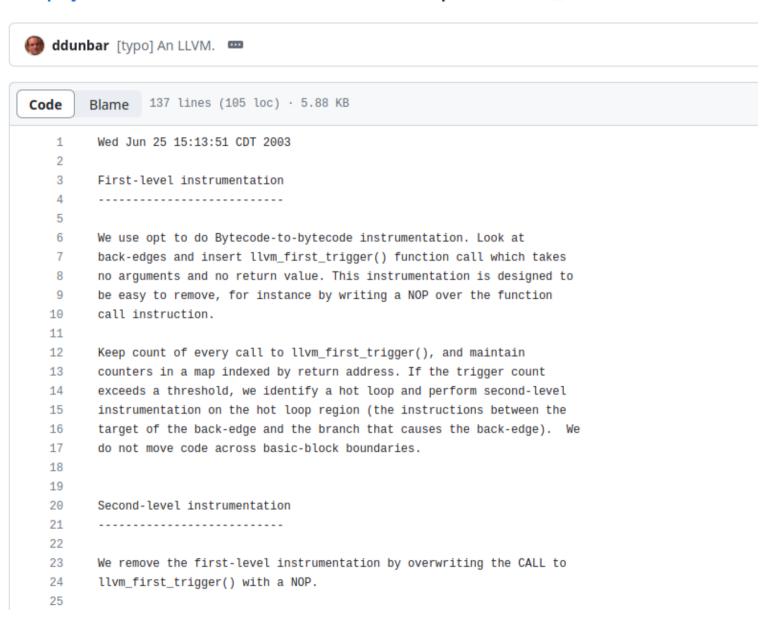
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- Runtime profile guided optimization
 - De-virtualization, instruction reordering, and other types of PGOs in ORC JIT
- Scientific computing (CERN)
 - Use high precision floating point for early iterations and use low precision floating point in later iterations for places that matter

REVIVING FEATURE FROM 2003?

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Ilvm-project / Ilvm / docs / HistoricalNotes / 2003-06-25-Reoptimizer1.txt 📮

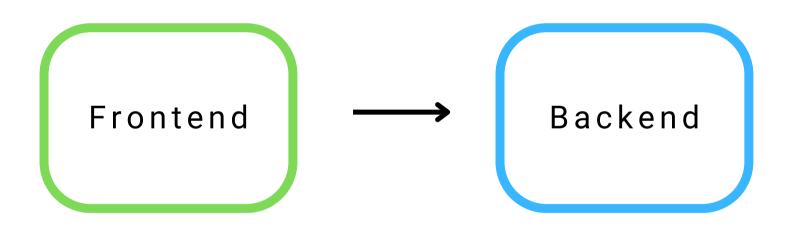


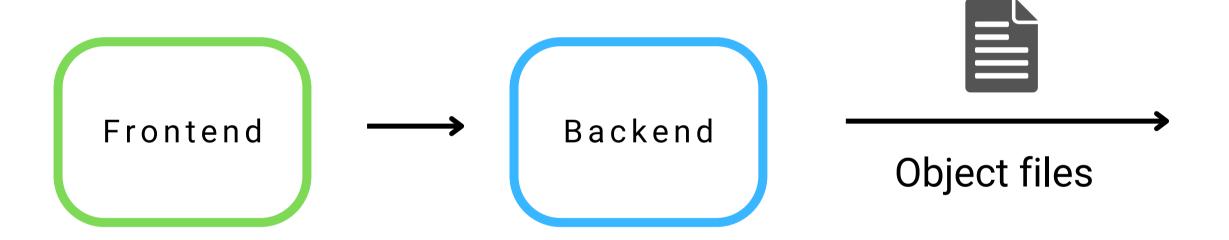
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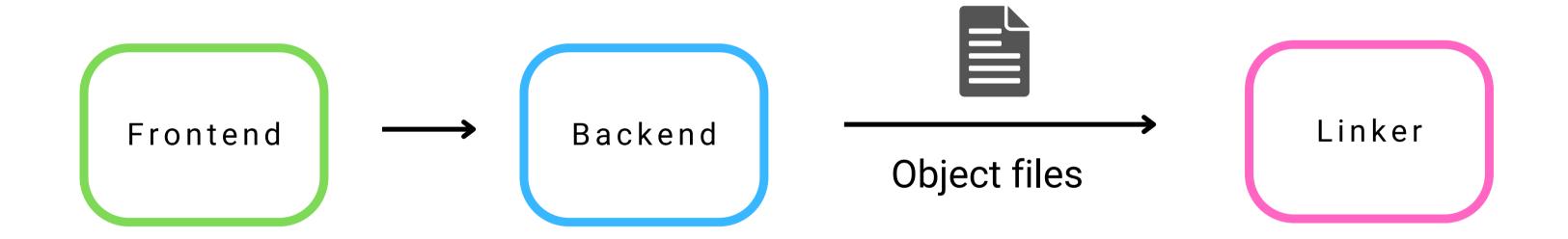
Ilvm-project / Ilvm / docs / HistoricalNotes / 2003-06-25-Reoptimizer1.txt ddunbar [typo] An LLVM. Blame 137 lines (105 loc) · 5.88 KB Code Wed Jun 25 15:13:51 CDT 2003 First-level instrumentation _____ We use opt to do Bytecode-to-bytecode instrumentation. Look at back-edges and insert llvm_first_trigger() function call which takes no arguments and no return value. This instrumentation is designed to be easy to remove, for instance by writing a NOP over the function call instruction. 11 Keep count of every call to llvm_first_trigger(), and maintain counters in a map indexed by return address. If the trigger count exceeds a threshold, we identify a hot loop and perform second-level instrumentation on the hot loop region (the instructions between the target of the back-edge and the branch that causes the back-edge). We do not move code across basic-block boundaries. 18 19 Second-level instrumentation -----22 We remove the first-level instrumentation by overwriting the CALL to 24 llvm_first_trigger() with a NOP. 25

Quite different but has the same name:)

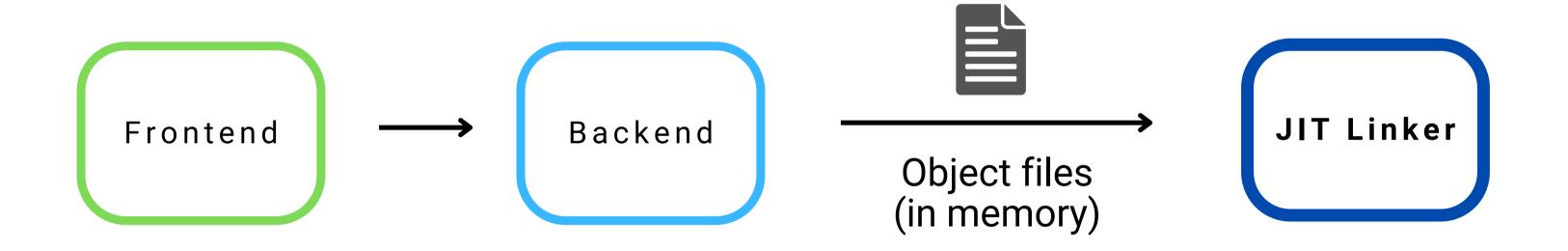




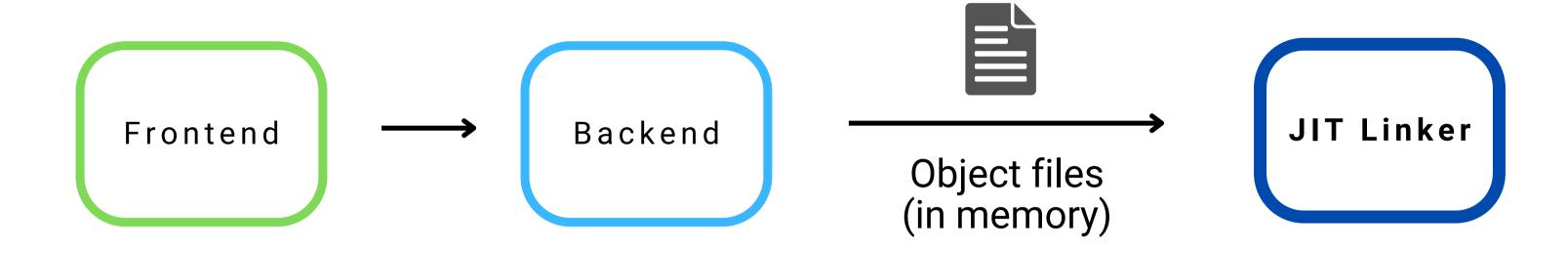




JIT execution pipeline in LLVM



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- Share a huge portion of pipeline with AOT
- Fewer breakage by LLVM internal code changes

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 Supports static initializer, thread local storage (TLS), and runtime symbol lookup ("dlload or dlsym" of JIT symbols)

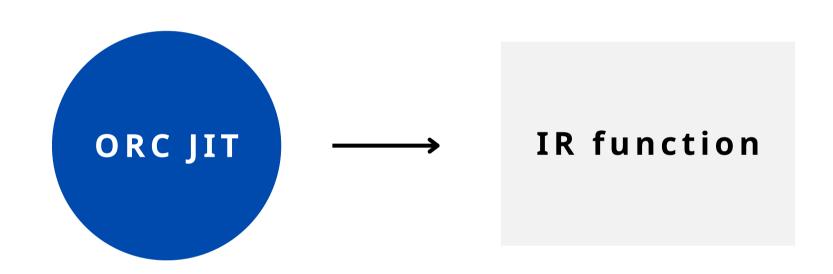
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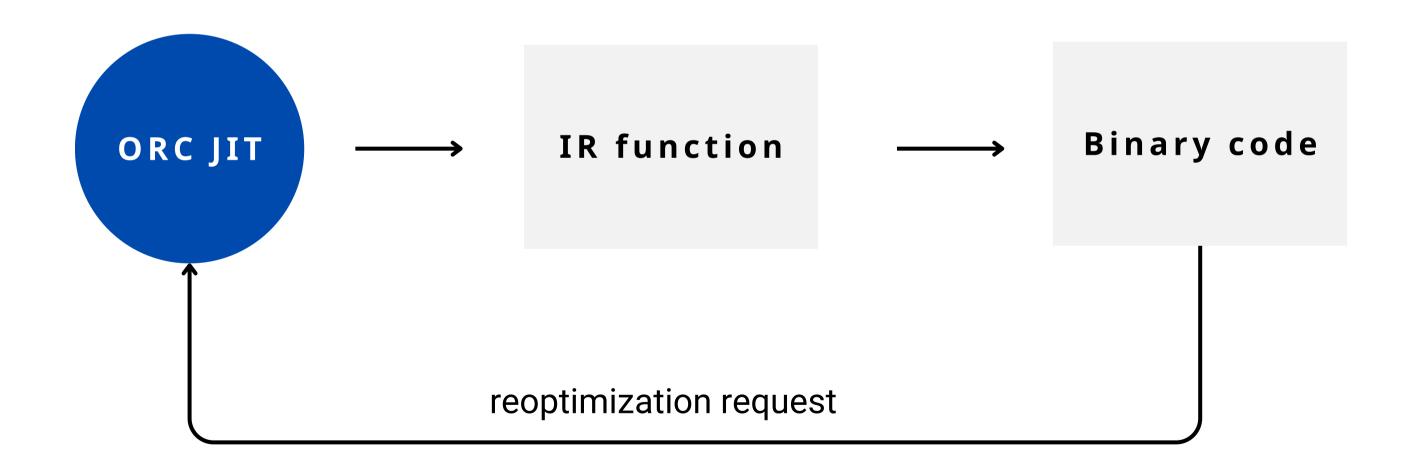
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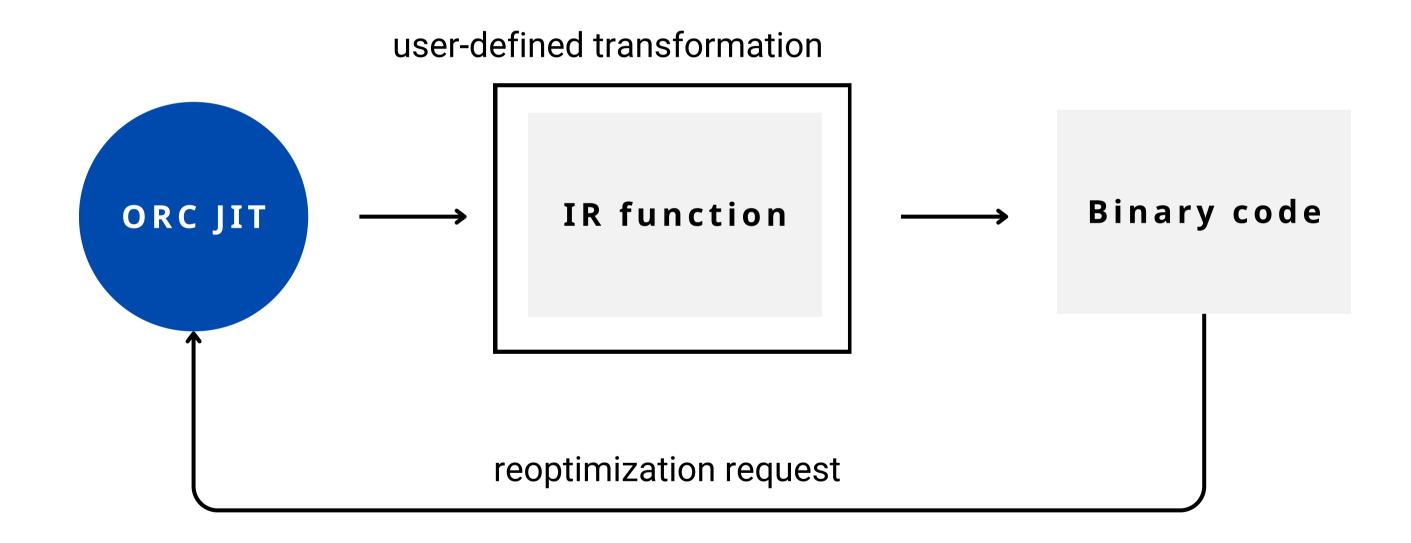
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- Multi-thread, remote process, speculative compilation ...

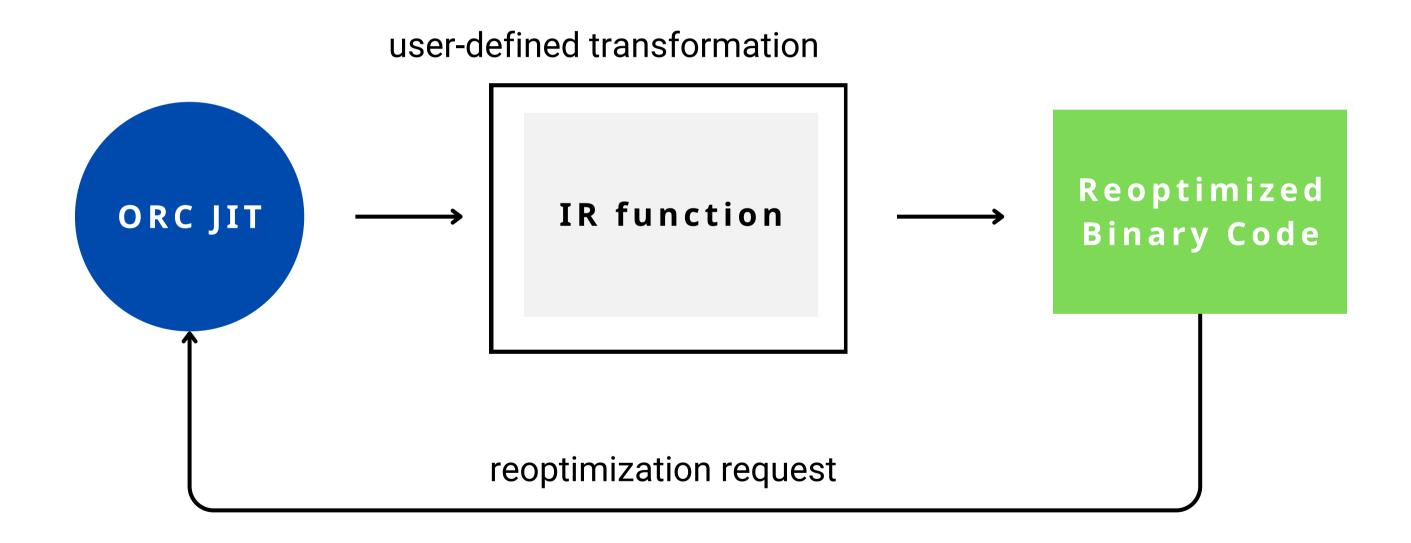












LLLayerJIT

• LLLayerJIT

```
std::unique_ptr<LLLayerJIT> Jit;
Jit->addLayer(ReOptLayer);
Jit->addLayer(std::make_unique<LLIRPartitionLayer>());
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ReOptimizeLayer

• Insert instrumentation code and re-optimization request code.

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static Error reoptimizeBasic(ReOptimizeLayer &Parent, ReOptMaterializationUnitID MUID,
    unsigned CurVerison, ResourceTrackerSP OldRT, ThreadSafeModule &TSM) {
    TSM.withModuleDo([&](llvm::Module &M) {
        // Do some re-optimization based on profile data
    });
    return Error::success();
}
auto ReOptLayer = std::make_unique<LLReOptimizeLayer>(ES, RSManager);
ReOptLayer->setReOptimizeFunc(reoptimizeBasic);
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- Default is "reoptimizelfCallFrequent" which requests re-optimization when call count is high.

Example: do -02 optimization if function was called more than 10

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static Error reoptimizeToO2(ReOptimizeLayer &Parent, ReOptMaterializationUnitID MUID,
    unsigned CurVerison, ResourceTrackerSP OldRT, ThreadSafeModule &TSM) {
    TSM.withModuleDo([&](llvm::Module &M) {
        auto PassManager = buildPassManager();
        PassManager.run(M);
    });
    return Error::success();
}
ReOptLayer->setReOptimizeFunc(reoptimizeToO2);
ReOptLayer->setAddProfilerFunc(reoptimizeIfCallFrequent);
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DEMO: CLANG-REPL WITH REOPT

- clang-repl is LLVM's in-tree c++ interpreter based on ORC JIT API
- The code originally from CERN's cling which has been used to analyze LHC data.

• Redirection to new symbol happens at JIT linker (JITLink) level

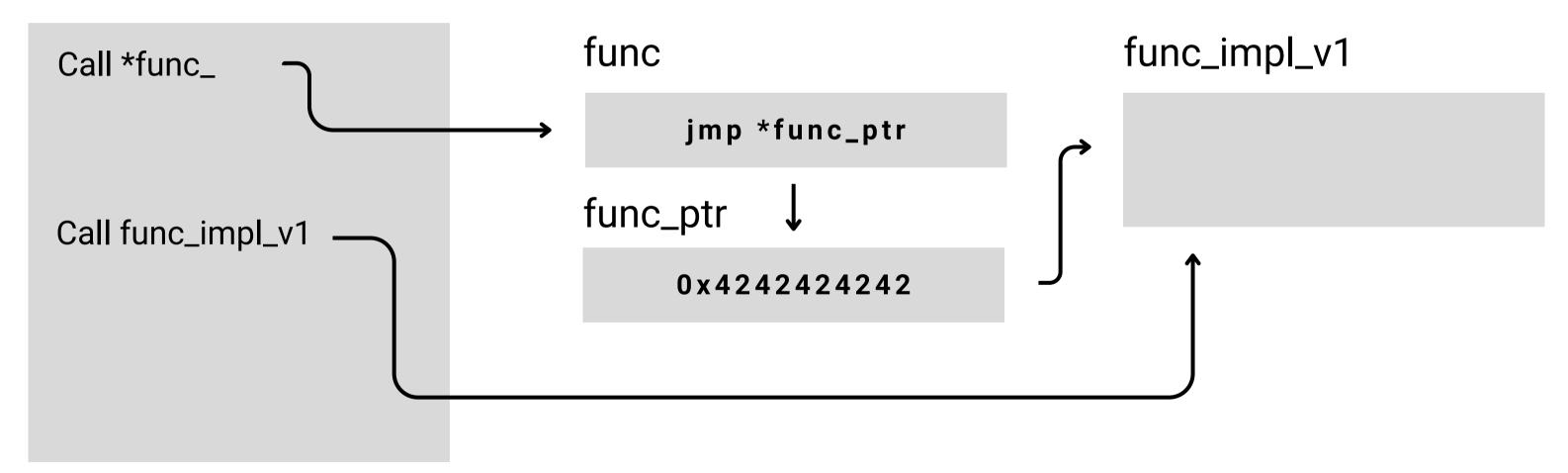
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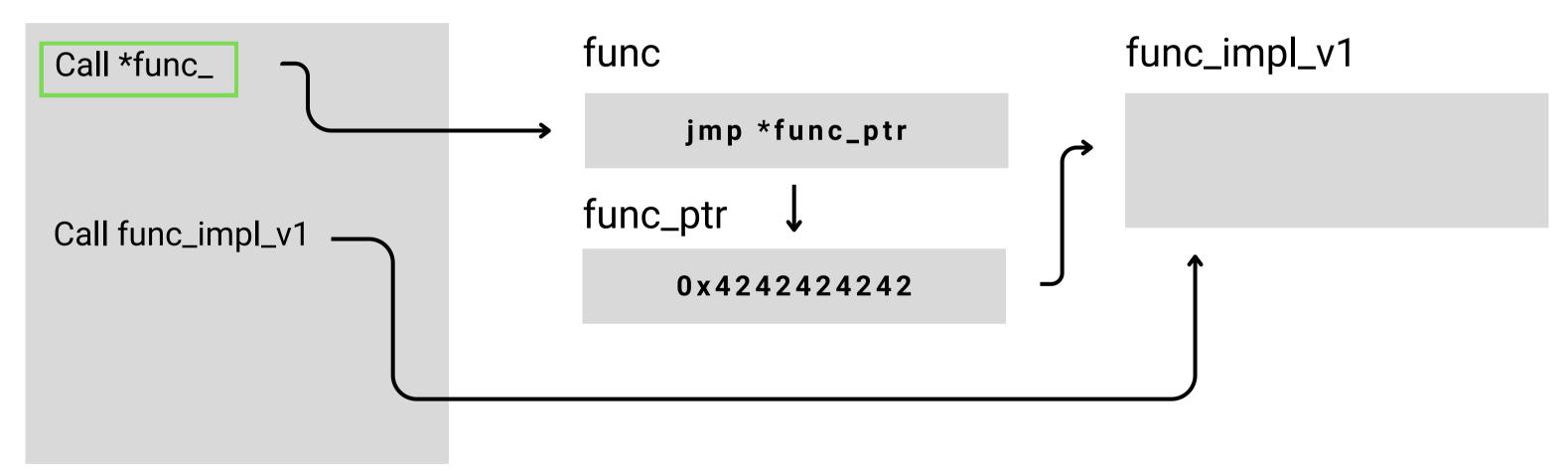
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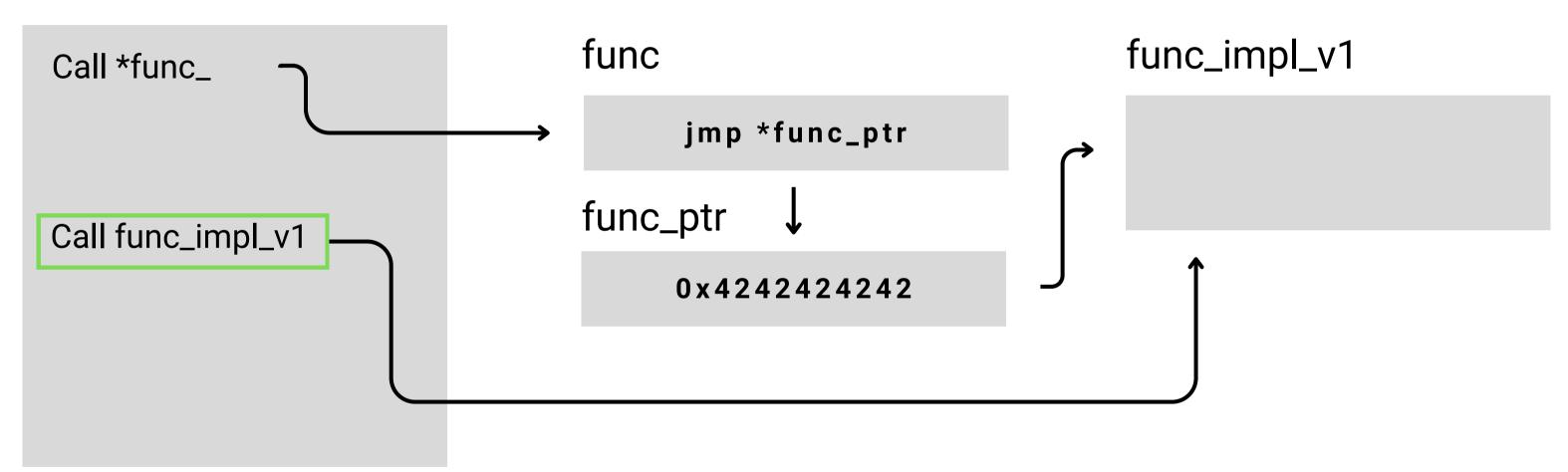
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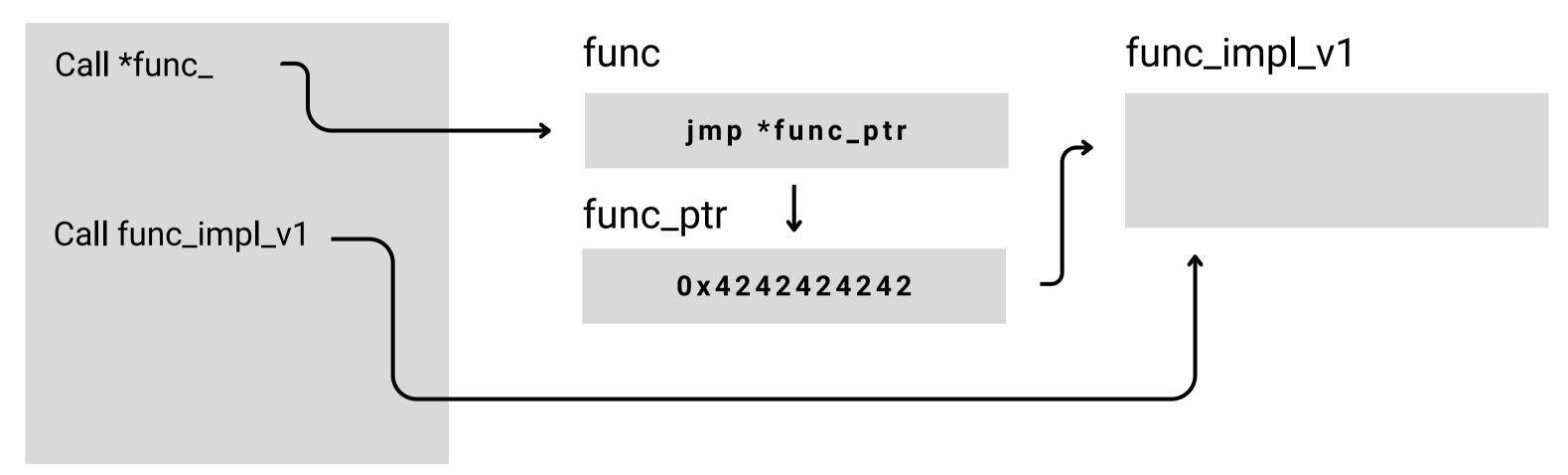
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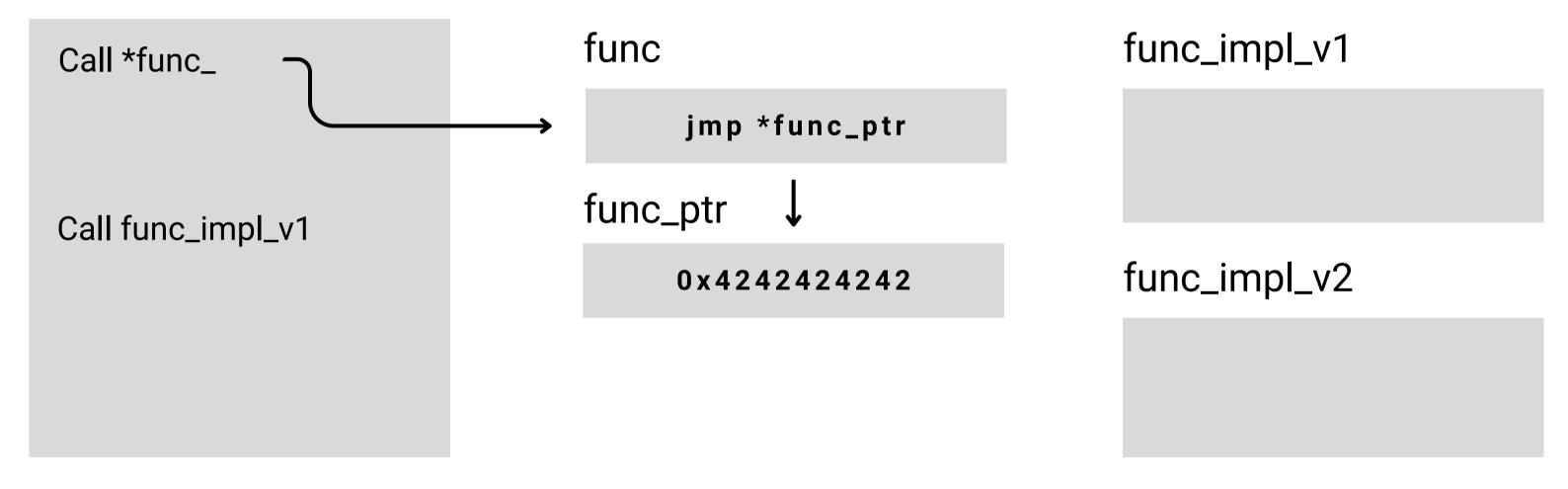
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 - when platform prevents writable and executable memory for security reason.

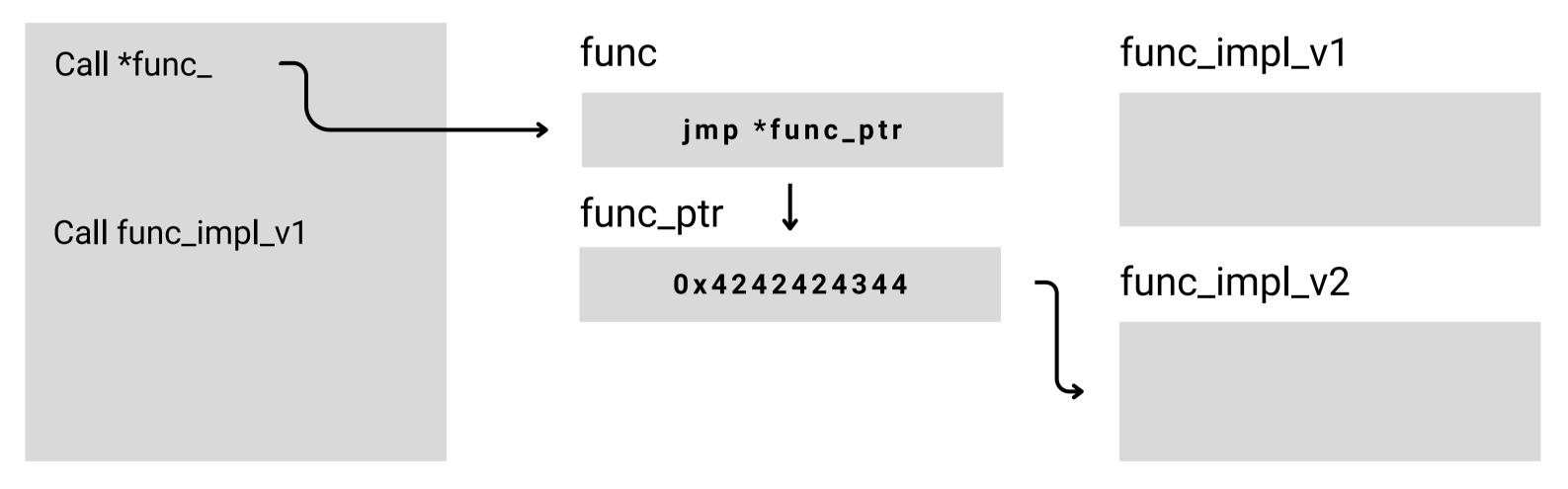


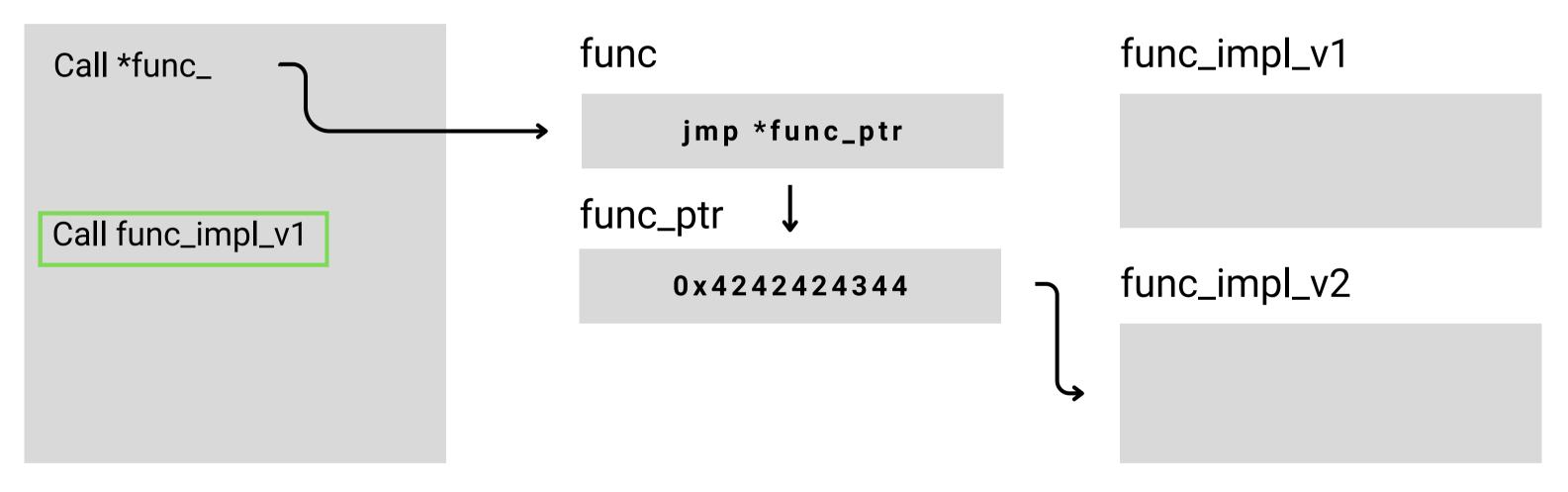


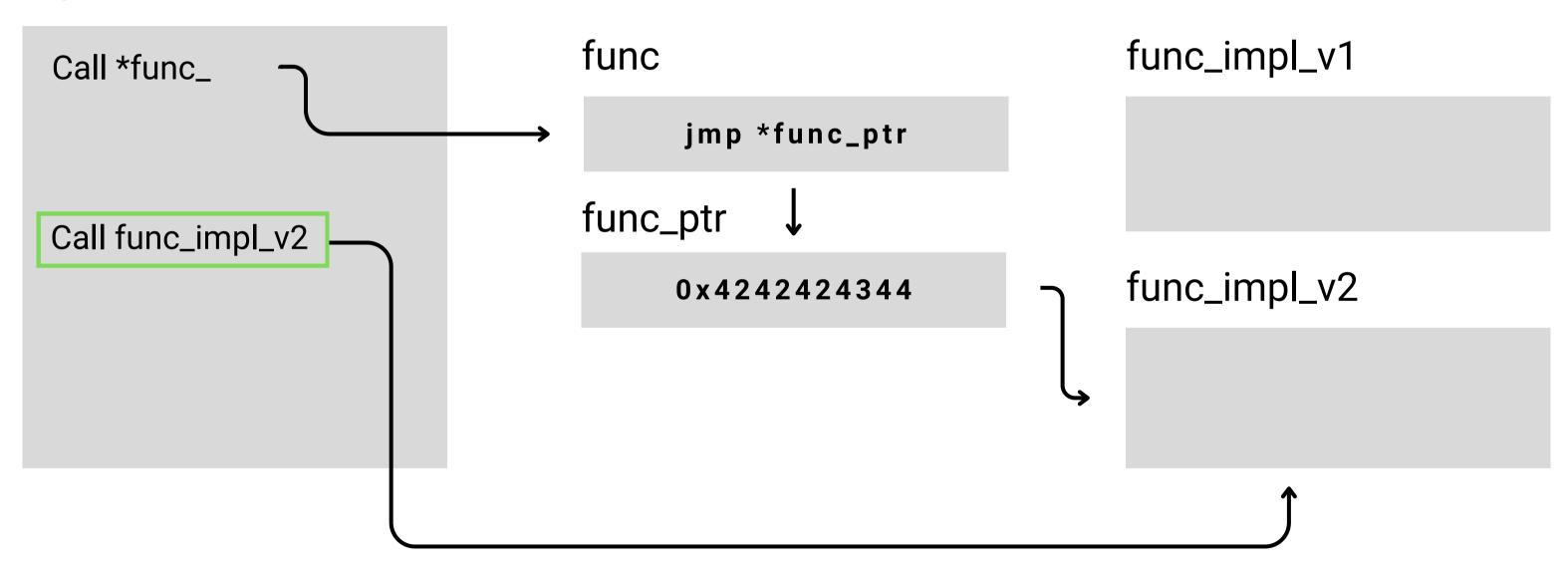












ADVANCED USAGE OF REOPTIMIZATION API

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```
define i32 @main() {
    %1 = alloca ptr
    %2 = load ptr, ptr %1
    %3 = load ptr, ptr %2
    %4 = getelementptr inbounds ptr, ptr %3, i64 0
    %5 = load ptr, ptr %4, align 8
    call void %5
    ret i32 0
}
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 - Not just indirection cost but also lose opportunity for potential optimizations as values are not within the same basic block

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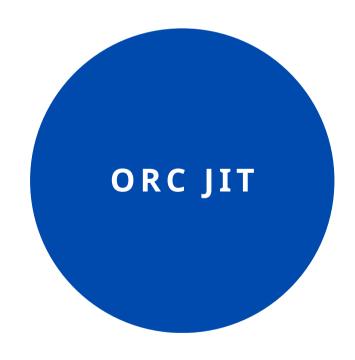


JITted code

call %1
__orc_rt_increment_func_callcnt(%1)
__ort_rt_reoptimize(1)

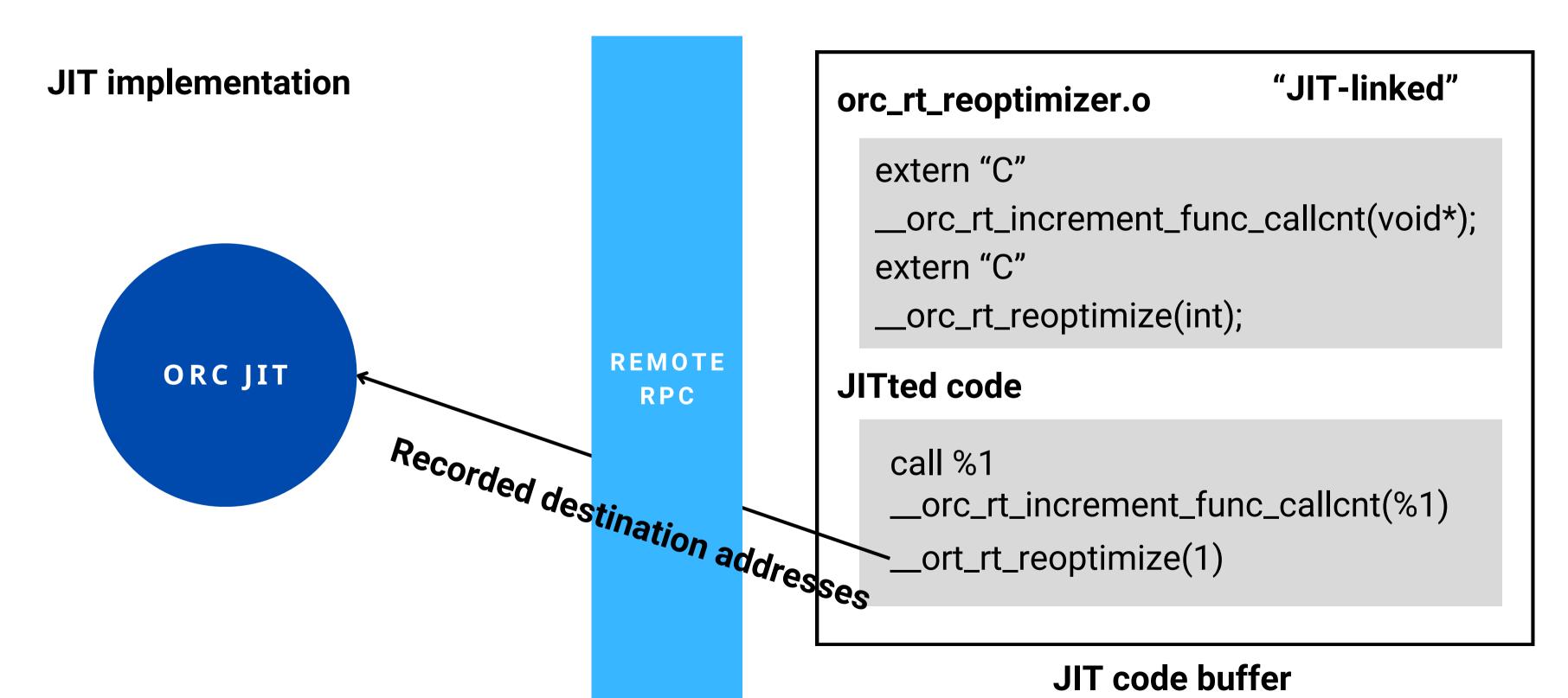
JIT code buffer

JIT implementation



```
"JIT-linked"
orc_rt_reoptimizer.o
  extern "C"
  __orc_rt_increment_func_callcnt(void*);
  extern "C"
  __orc_rt_reoptimize(int);
JITted code
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   __orc_rt_increment_func_callcnt(%1)
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```

JIT code buffer



DEMO: CLANG-REPL WITH DEVIRTUALIZATION

Showcasing the de-virtualization within clang-repl

Program	-01	Reoptimization ON	-02
Boost Spirit (n=1)	1.97s	2.12s	2.24s
Boost Spirit (n=500)	22.46s	21.71s	21.55s

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Ray Tracer	158.9s	66.6s	66.0s	62.5s

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- The runtime performance drop observed to be as bad as 3x slower.
- Current solution: don't delete function when splitting module but just mark them externally_available.
 - but this introduces compilation overhead when module is large
 - O(n^2) function duplicates where n is number of functions

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- Look into optimizing function with a huge loop up front
 - The penalty we get when we couldn't re-optimize certain function are substantial
 - Penalty = cost for instrumentation + lost optimizations
- Generic JIT profile guided optimization framework
 - Could we possibly overhaul LLVM's existing PGO infrastructure in order to reuse it?

THANKS

Code used today is available at: https://github.com/sunho/LLVM-JIT-REOPT-Example